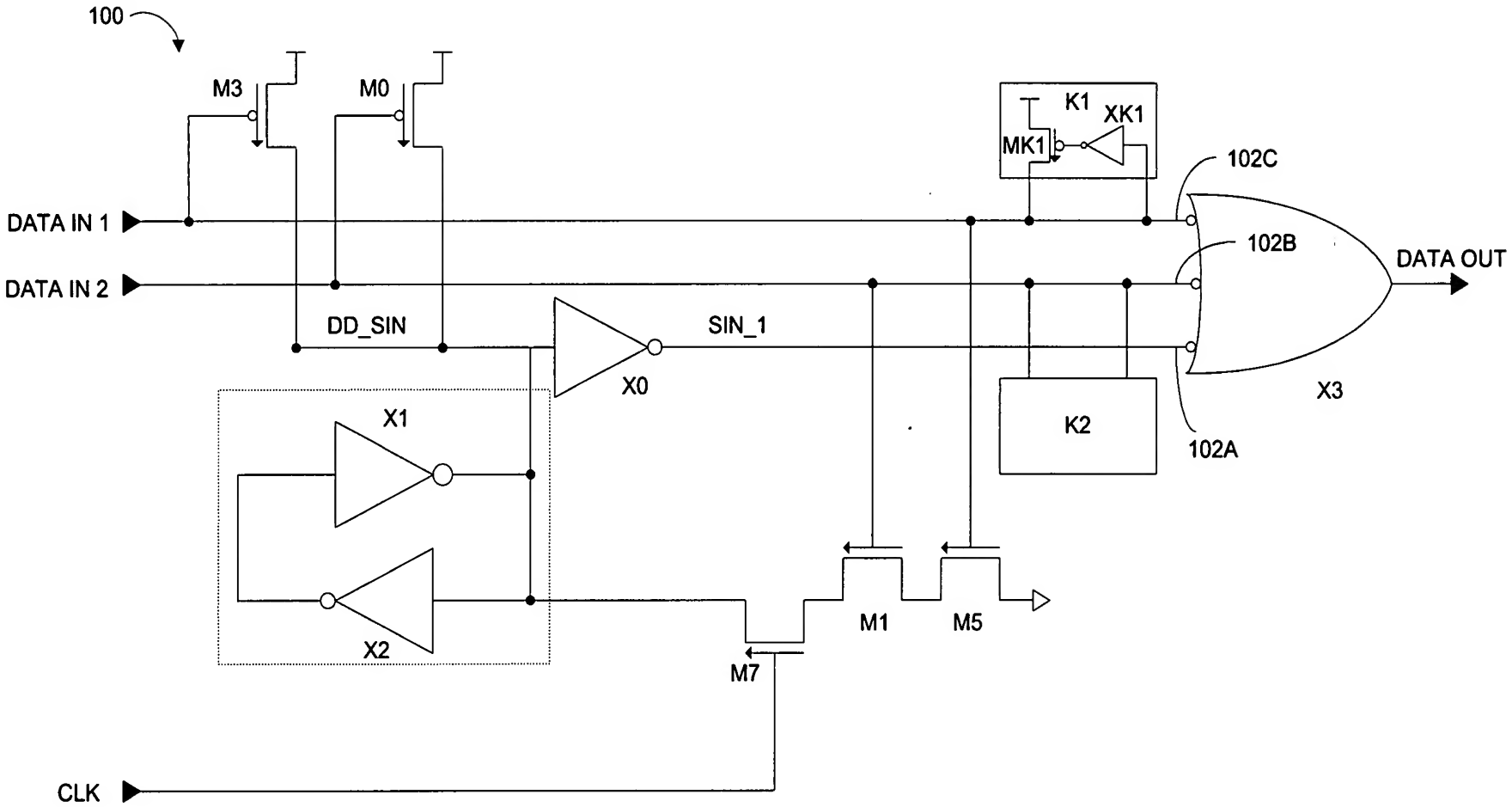

FIGURE 1
PRIOR ART

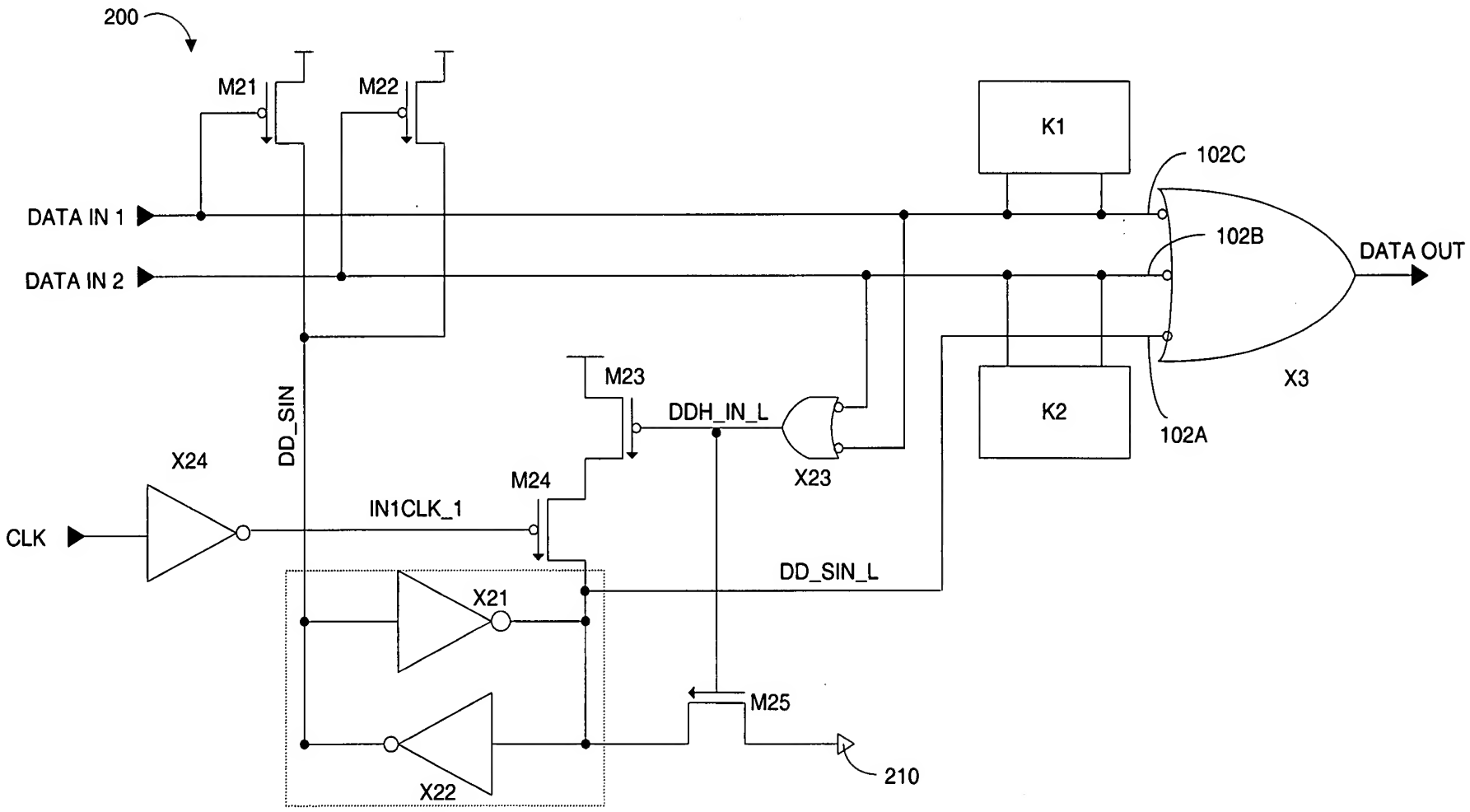
200
M21
M22
K1
102C
DATA IN 1
DATA IN 2
102B
DATA OUT
M23
DDH_IN_L
K2
X3
DD_SIN
X24
M24
X23
102A
IN1CLK_1
CLK
DD_SIN_L
X21
M25
210
X22

FIGURE 2

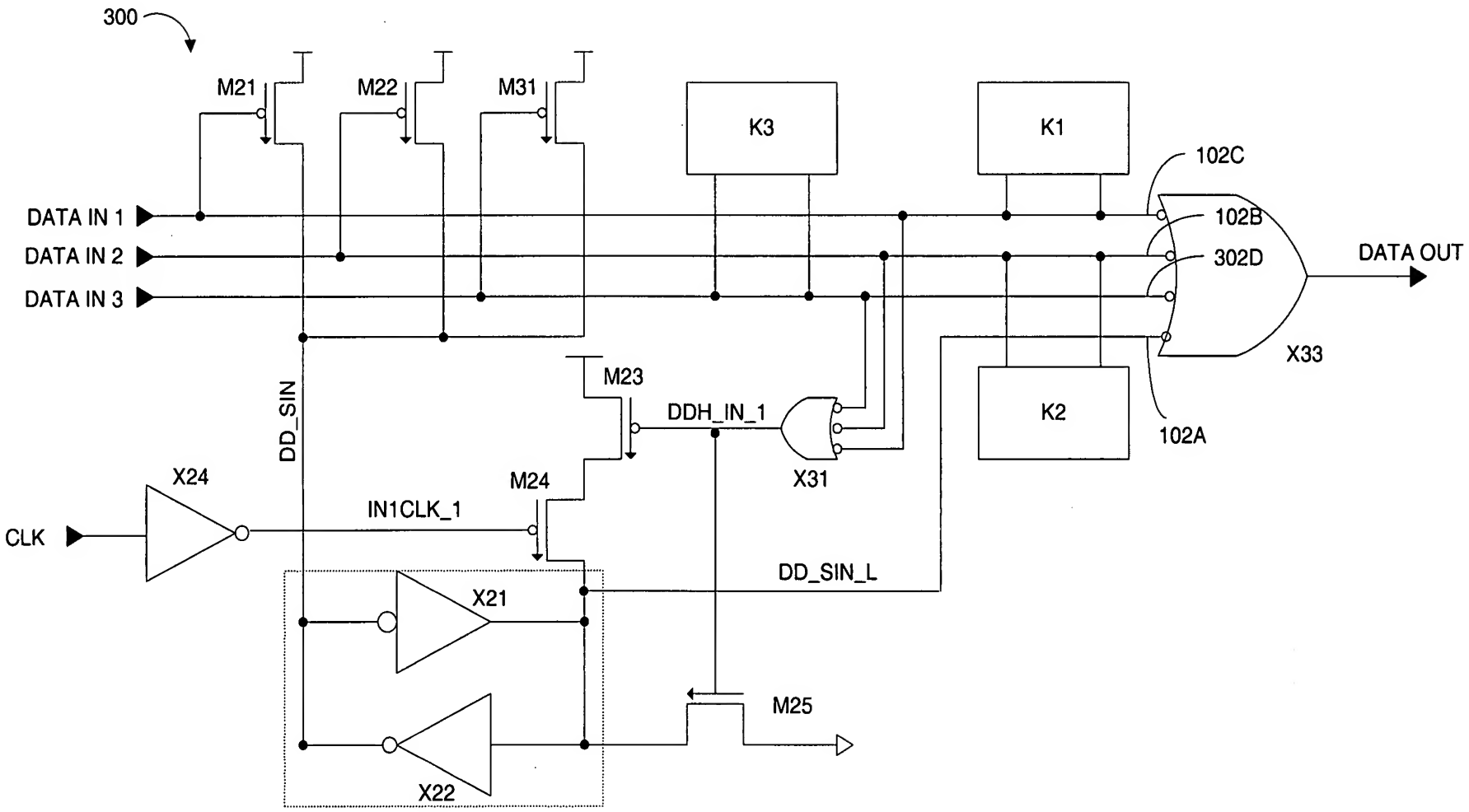
300
M21
M22
M31
K3
K1
102C
DATA IN 1
DATA IN 2
DATA IN 3
102B
302D
DATA OUT
X33
M23
DD_SIN
DDH_IN_1
K2
102A
X24
M24
X31
IN1CLK_1
CLK
DD_SIN_L
X21
M25
X22

FIGURE 3

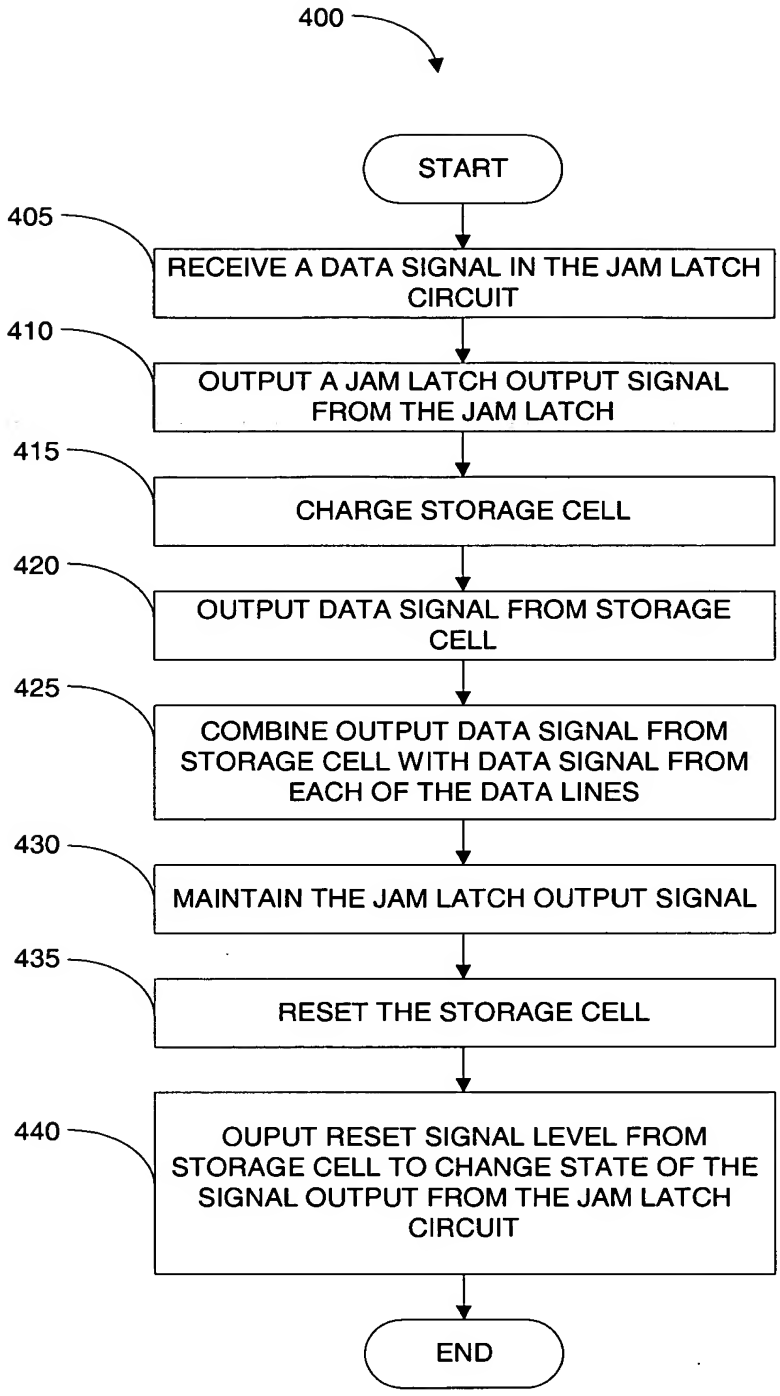
400
START
405
RECEIVE A DATA SIGNAL IN THE JAM LATCH CIRCUIT
410
OUTPUT A JAM LATCH OUTPUT SIGNAL FROM THE JAM LATCH
415
CHARGE STORAGE CELL
420
OUTPUT DATA SIGNAL FROM STORAGE CELL
425
COMBINE OUTPUT DATA SIGNAL FROM STORAGE CELL WITH DATA SIGNAL FROM EACH OF THE DATA LINES
430
MAINTAIN THE JAM LATCH OUTPUT SIGNAL
435
RESET THE STORAGE CELL
440
OUPUT RESET SIGNAL LEVEL FROM STORAGE CELL TO CHANGE STATE OF THE SIGNAL OUTPUT FROM THE JAM LATCH CIRCUIT
END

FIGURE 4A

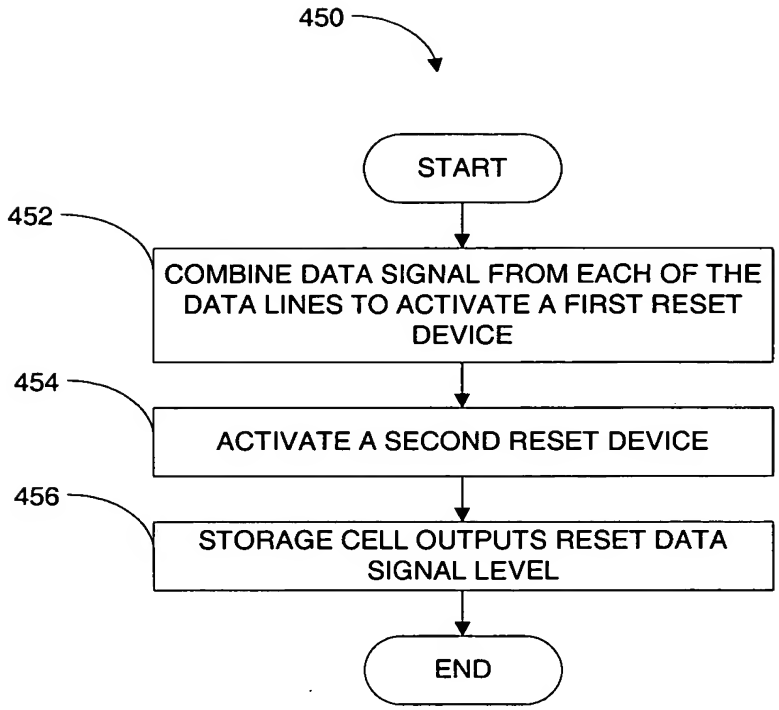
450
START
452
COMBINE DATA SIGNAL FROM EACH OF THE DATA LINES TO ACTIVATE A FIRST RESET DEVICE
454
ACTIVATE A SECOND RESET DEVICE
456
STORAGE CELL OUTPUTS RESET DATA SIGNAL LEVEL
END

FIGURE 4B

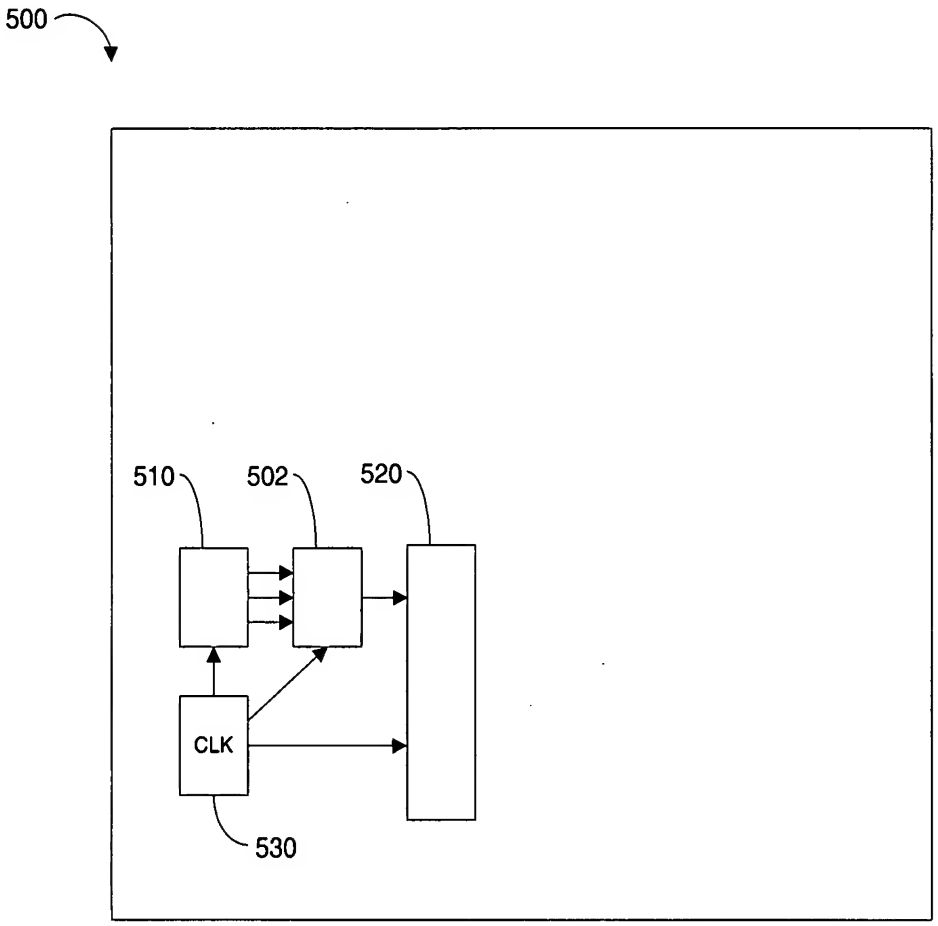

FIGURE 5